

+

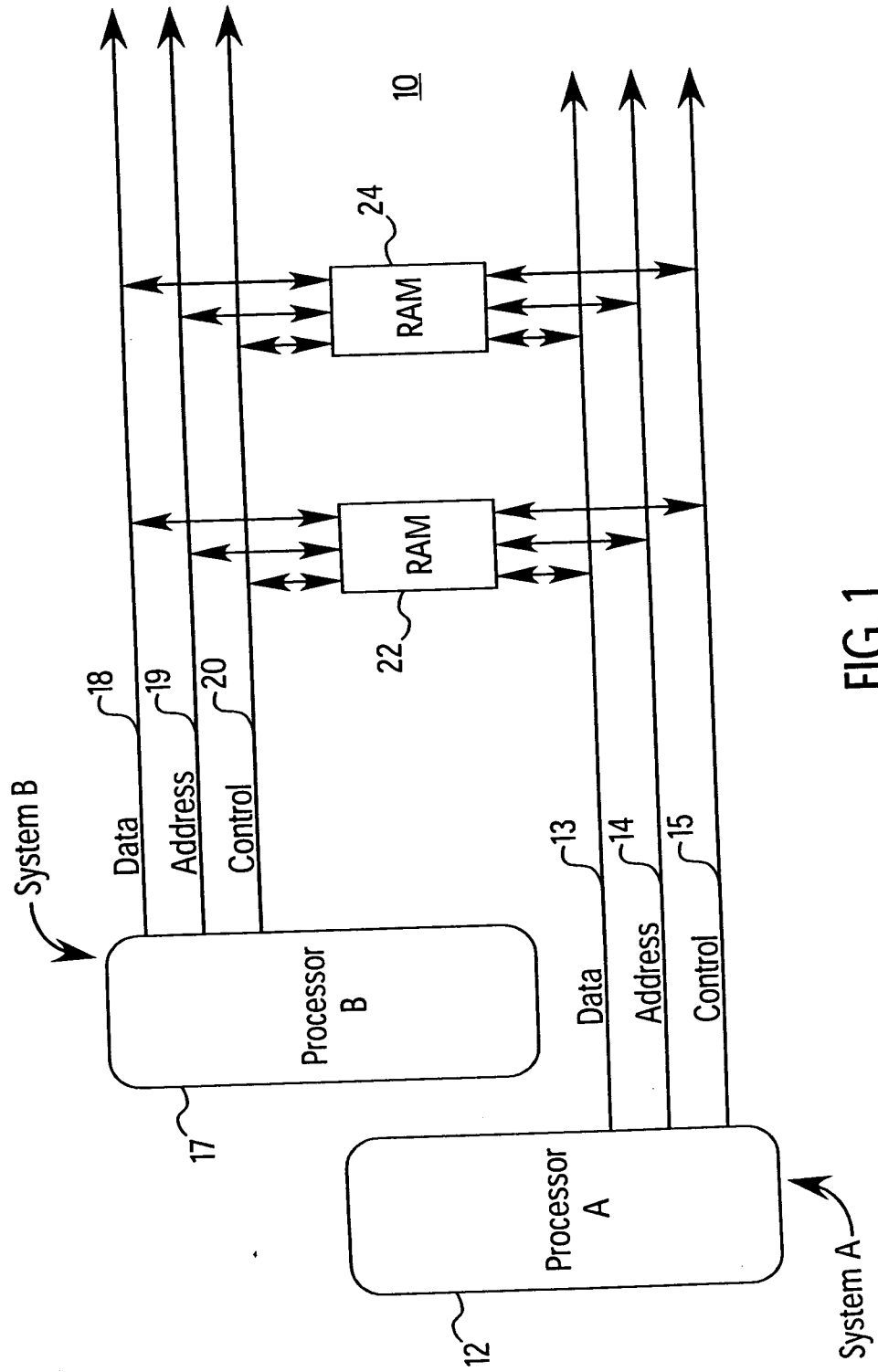


FIG. 1

+

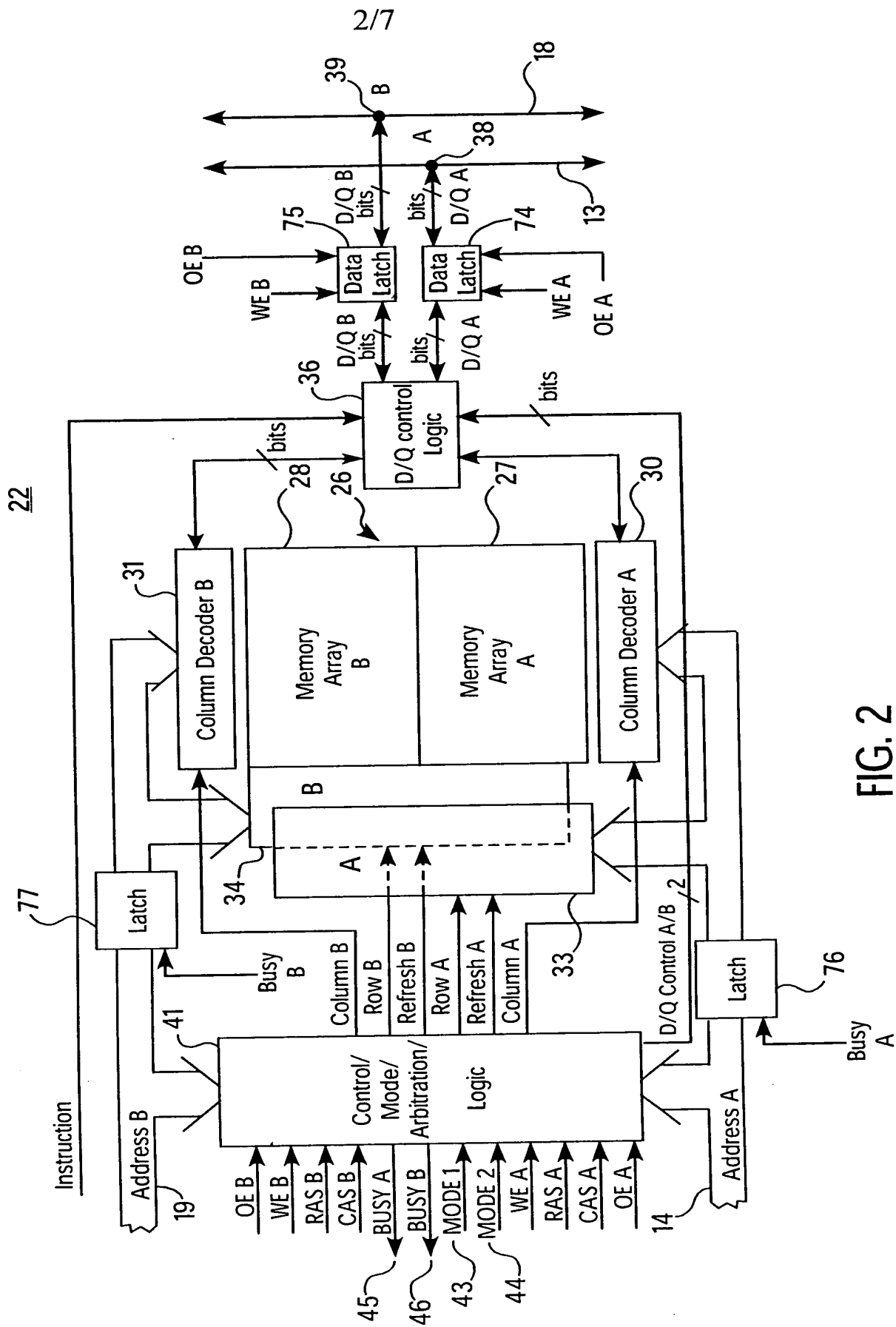


FIG. 2

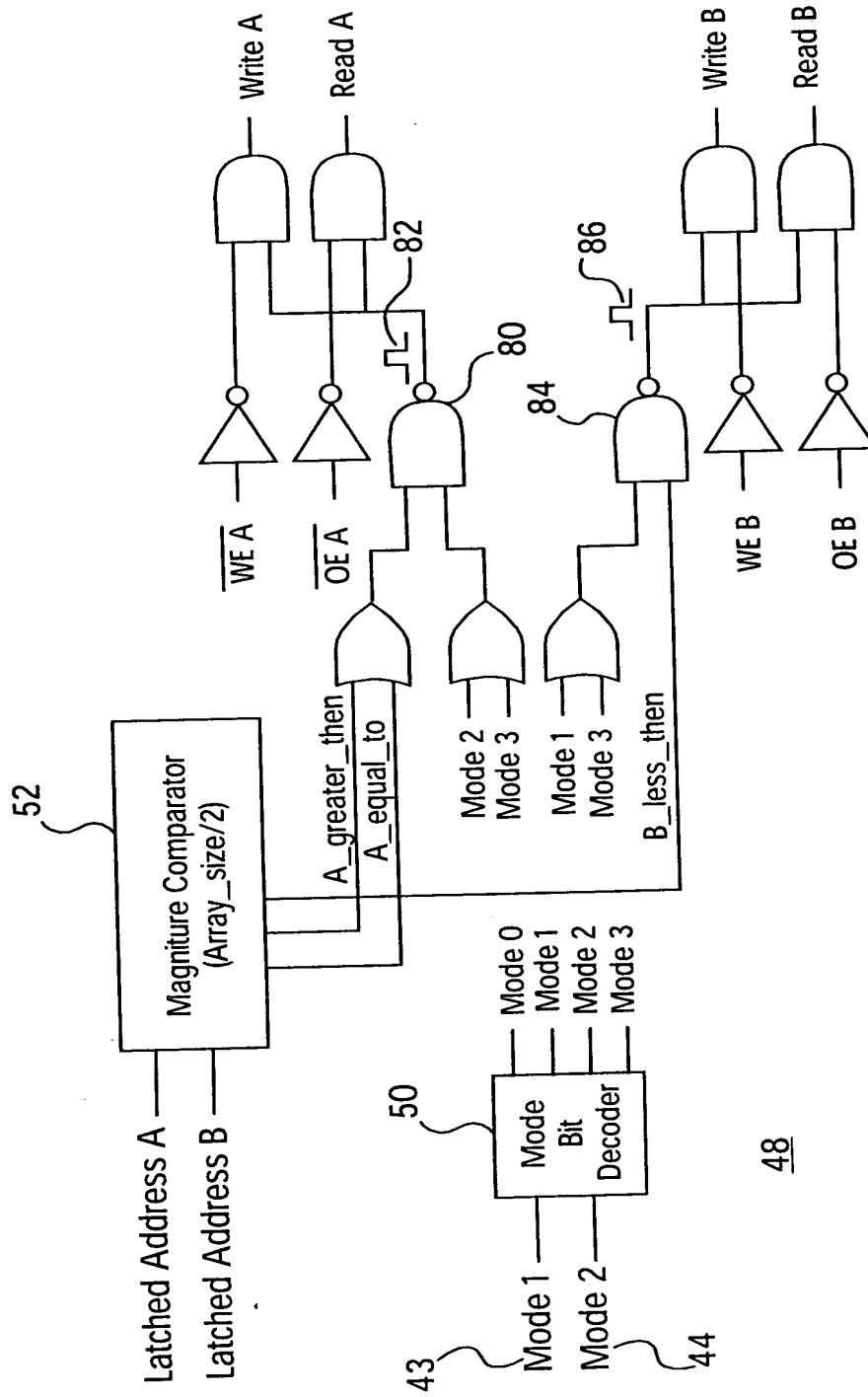


FIG. 3

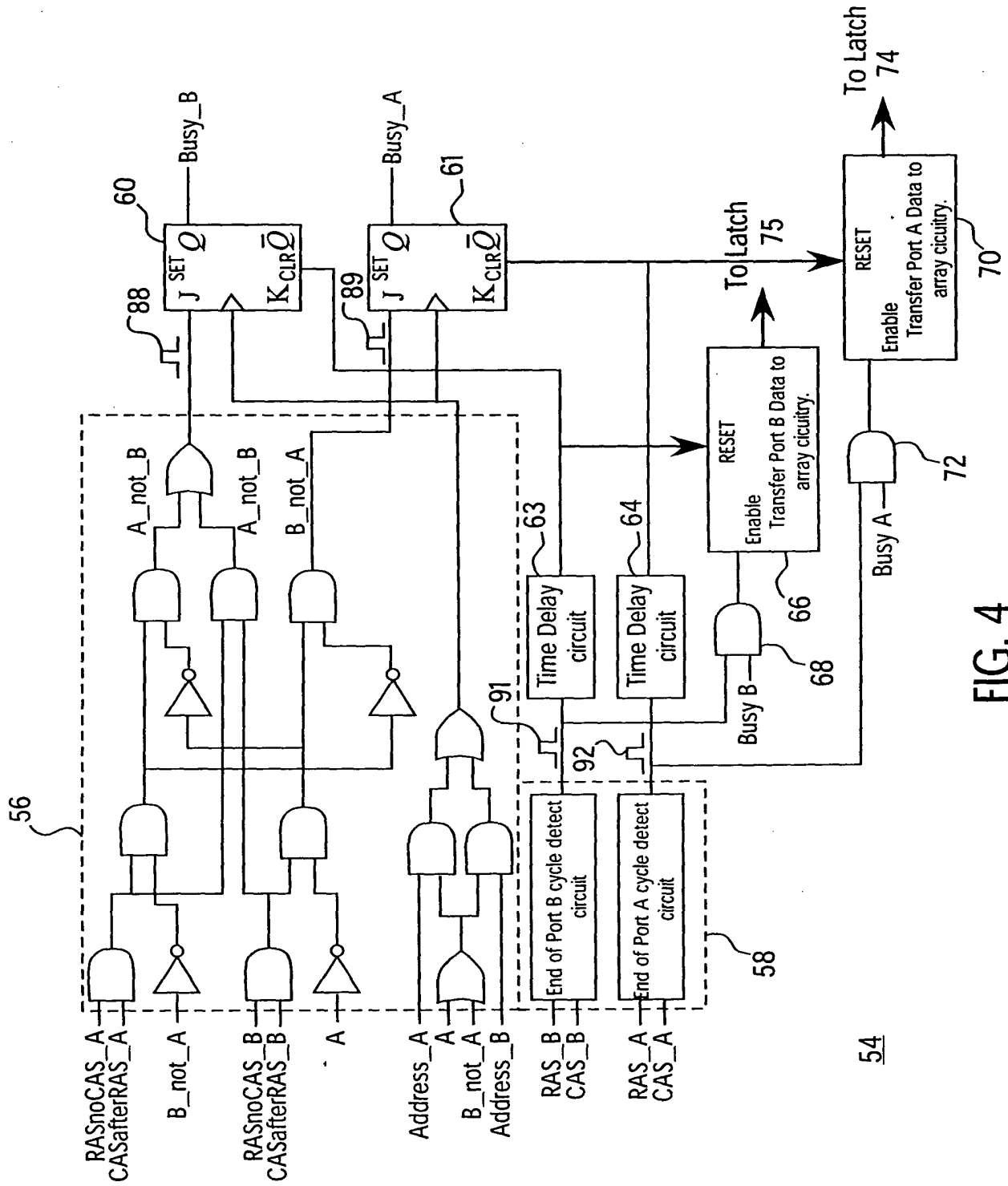


FIG. 4

Simultaneous
Access

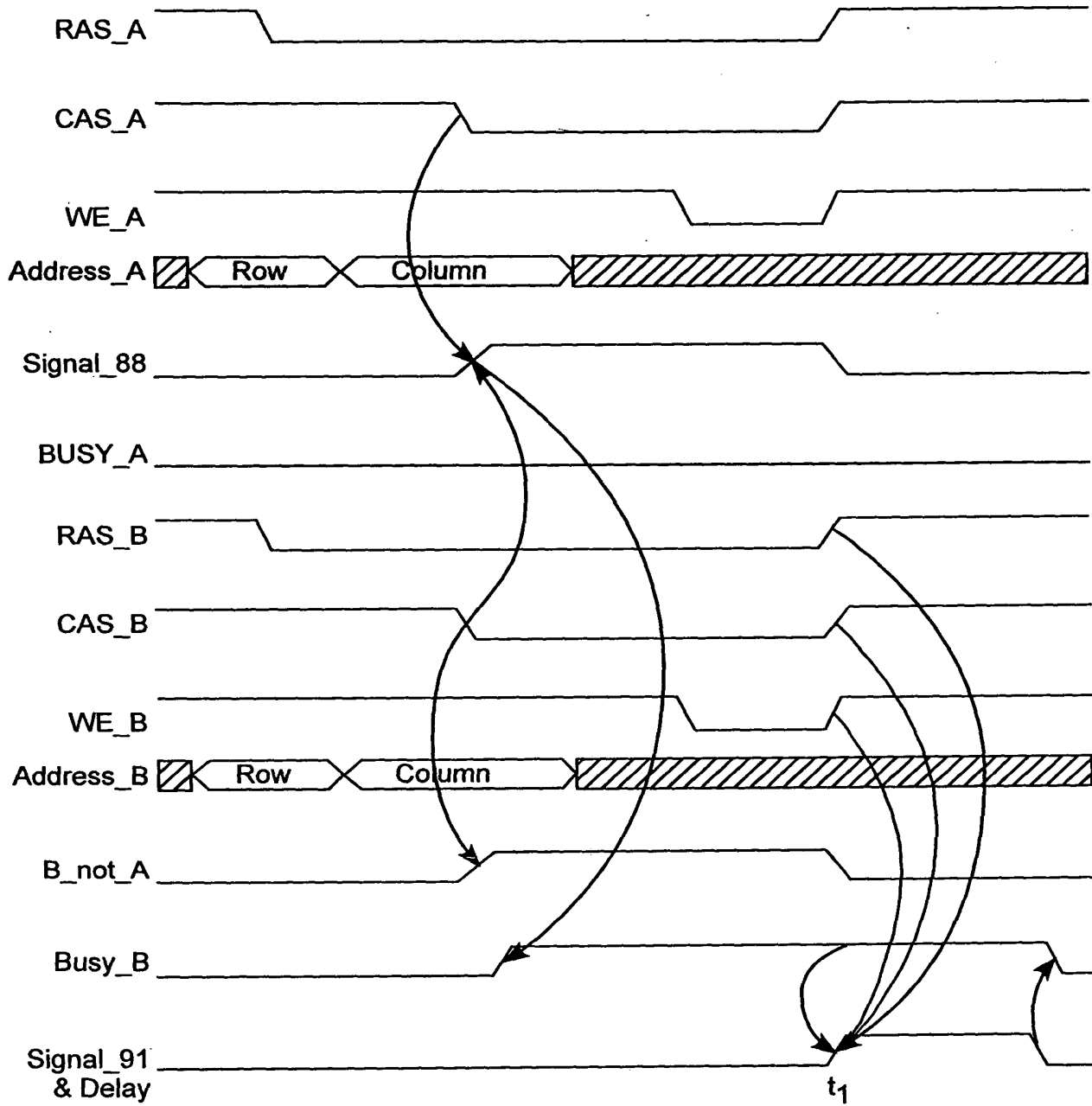


FIG. 5

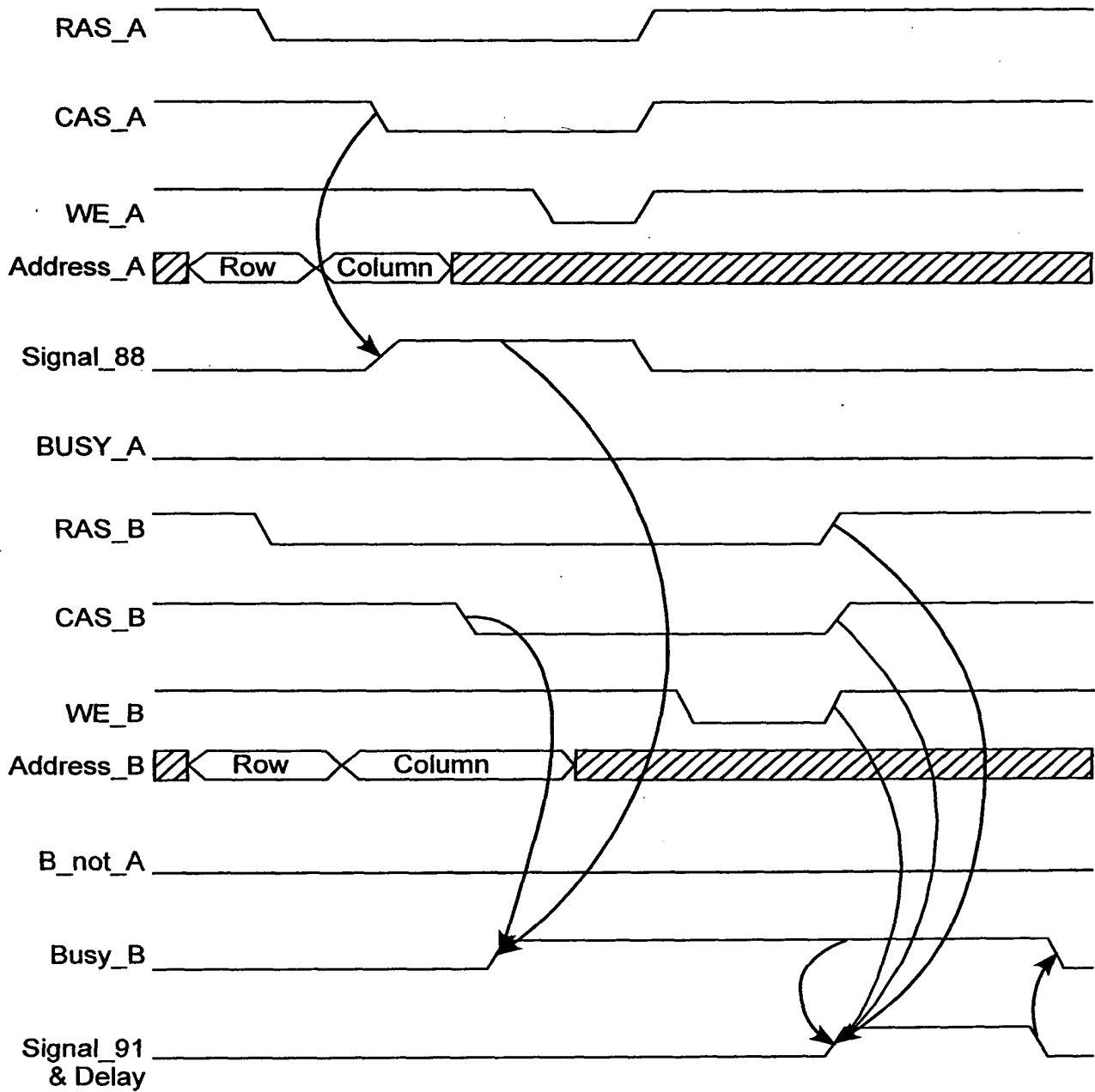
Sequential
Access

FIG. 6

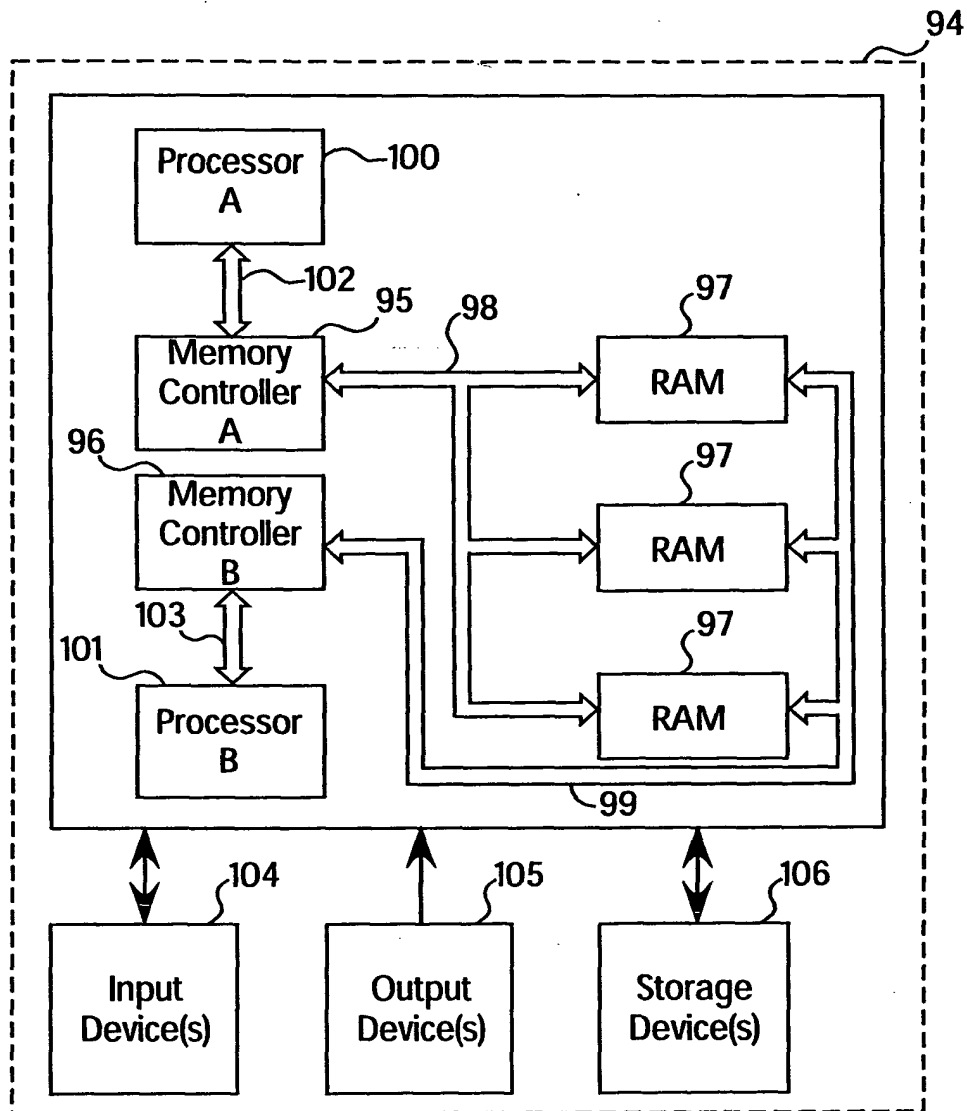


FIG. 7